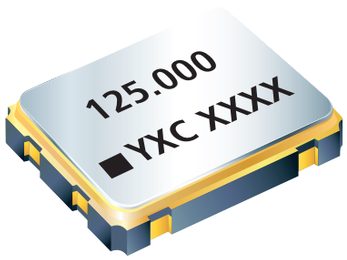
YSO752ST



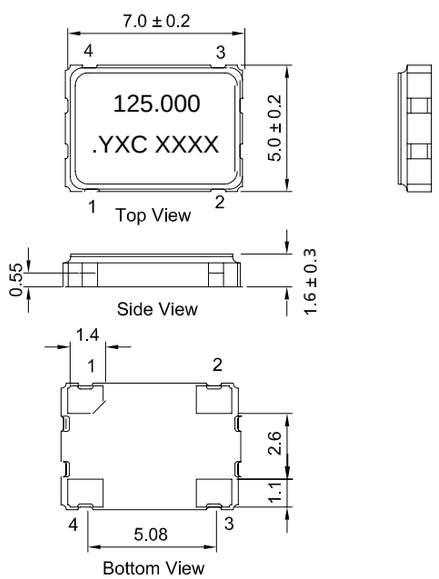
Features

- External dimensions: 7.5 x 5.0 x 1.6 mm.
- Frequency range: 1MHz ~ 150MHz.
- Smallest & Thinest SMD package.
- Excellent temperature range, Best Reliability.
- Excellent total frequency stability.
- Applications: WLAN, Bluetooth, DSC, DSL, IP CAM, SSD, e-books and other IT product.

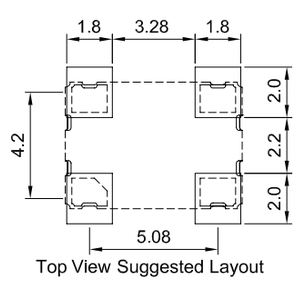
Electrical Specifications

Item / Type		YSO752ST			
Output Frequency Range		1~90MHz		90~150MHz	
Supply Voltage		1.8V	2.25~5.5V	1.6~3.6V	5.0V
Current Consumption (Max.)		18mA	46mA	48mA	80mA
Output Type		CMOS			
Oscillation Mode		Fundamental / 3rd Overtone			
Frequency Tolerance		±20ppm, ±25ppm, or specify			
Output Load		15pF, or specify			
Operating Temperature Range		- 40 ~ + 85 ℃, or specify			
Storage Temperature Range		- 55 ~ + 125 ℃			
Voltage Vol (Max.) / Vol (Min.)		90%Vdd min./10%Vdd max			
Symmetry		45~55%DT,40/60~60/40%Svm			
Rise (Tr) / Fall (Tf) Time		10Max./8 ns Max.			
Start-up Time		10 ms Max.			
Aging (at 25℃)		± 3 ppm / year Max.			
Phase Noise(Pn)		-125 Max. dBc/Hz @1khz			
Three State Function	Enable Voltage	70%Vdd Min.			
	By Low Pressure	30%Vdd Max.			

Dimensions



引脚功能定义		
引脚	功能	定义
1	E/D	三态 / 无三态
2	GND	接地
3	OUTPUT	输出
4	Vdd	电源电压



Units: mm

YSO752ST

★ PART NUMBER GUIDE 部件号示例 *e.g. O7050125MHEA4SI* *YSO752ST=7.0×5.0 SMD SEAM TYPE*

Quartz Crystal Oscillator	Dimensions	Frequency (Hz)	Supply voltage (V)	Frequency Stability Overall (ppm)	output	Pin	Material	Operating Temp. Range
O	7050	125M	H	E	A	4	S	I

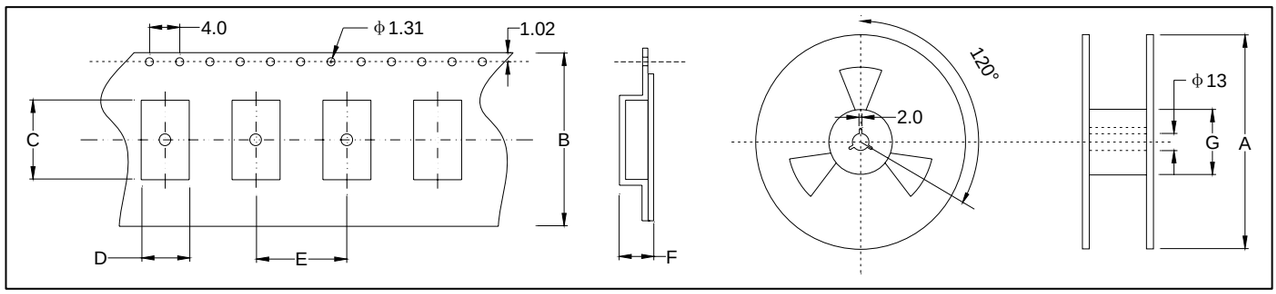
Supply voltage	
A	1.8V
E	3.3V
F	5.0V
G	3.3Vto5V
H	2.25Vto5.5V

Frequency tolerance	
D	±20PPM
E	±25PPM
H	±50PPM

Operating temperature	
C	-20 to 70℃
I	-40 to 85℃
F	-40 to 105℃
A	-40 to 125℃

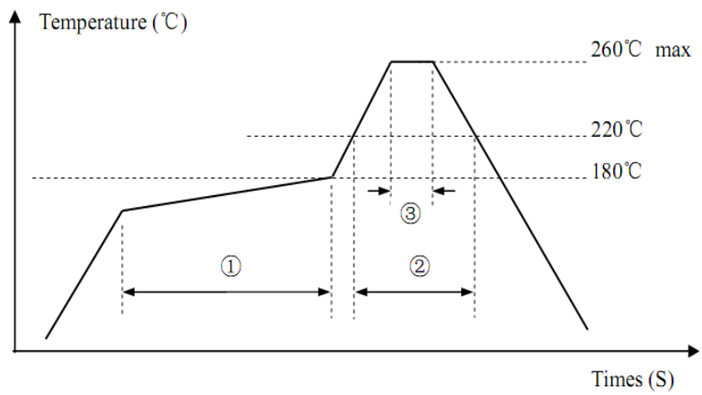
Output	
A	CMOS

★ TAPING SPECIFICATION (Unit: mm) 编带规格



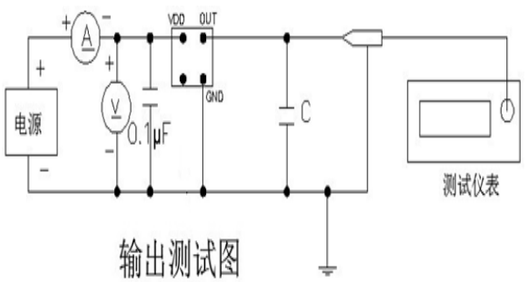
	A	B	C	D	E	F	G
OSC-SMD7050	178±2.0	16.0±0.3	8.40±0.15	5.80±0.15	7.88±0.1	1.90±0.1	58.0±1.0
1000 pcs per reel							

★ REFLOW SOLDERING PROFILE 回流焊特性



Pb free reflow A	①	Preheat	160~180℃	120sec. max
	②	Primary heat	220℃	60sec. max
	③	Peak	260℃	10sec. max.

★ Test Circuit 测试电路



输出测试图